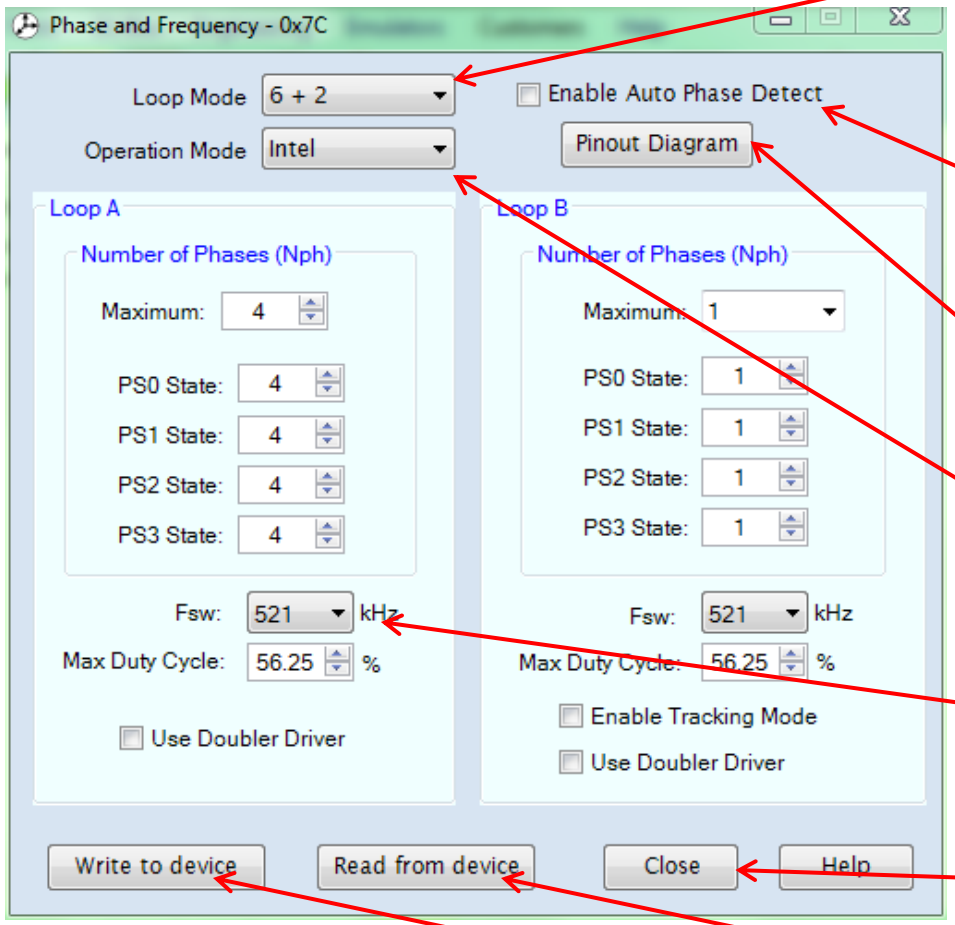
Phase & Frequency...



Loop Mode: Here the maximum number of phases can be selected. Depending on which partnumber there will be more or less combinations to select from.

Enable Auto Phase Detect: Mark if the automatic detection of number of mounted phases on a board is to be used

After selection of phases the **Pinout Diagram** button can display what pins are assigned to which signal

Operation MODE: Select type of application.

POL/telecom is intended for ASIC and FPGA loads that not follow Intel standards

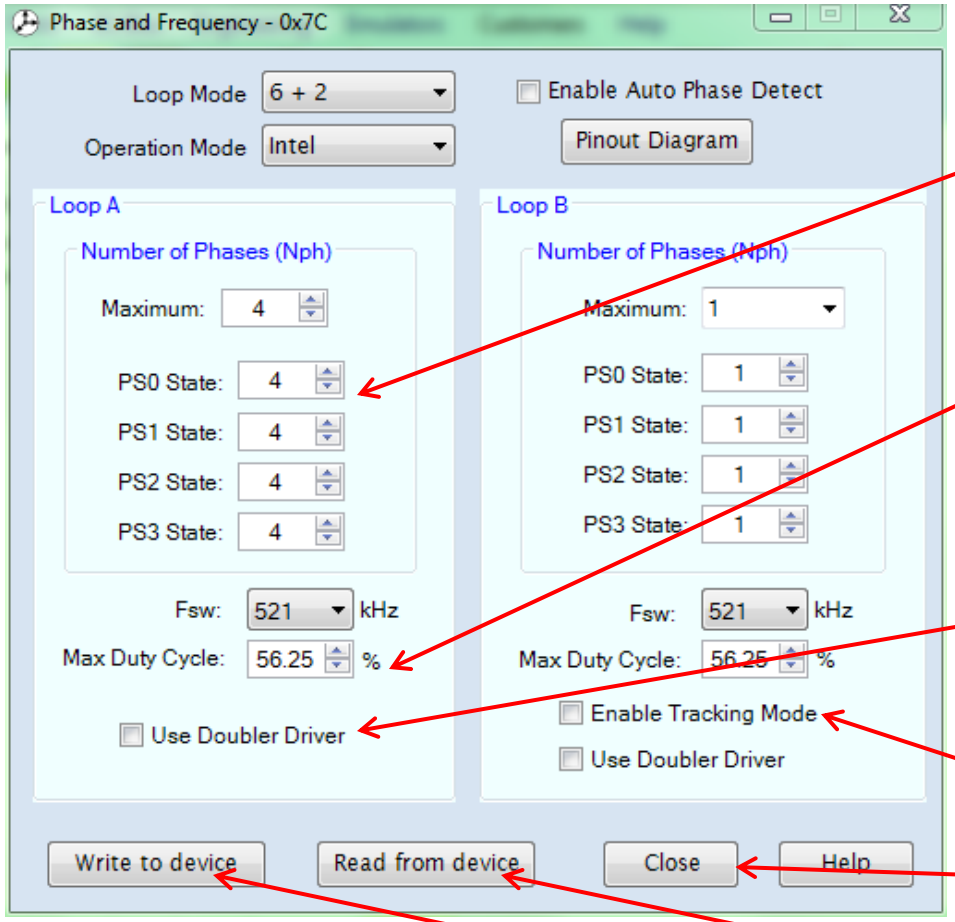
Fsw: opens a menu with different switching frequencies to select from. Note that depending on number of phases in a loop available frequencies may differ slightly due to internal division ratios.

Close button: close window without saving

Read from Device Button: Restore the values entered to values before they got changed or read from a device if connected.

Write button: save the settings

Phase & Frequency...



PSx State: Number of phases for the different PowerStates. Typically used for Intel appliacations. If not used enter same number as for Maximum phases

Max Duty cycle: Maximum dutycycle allowed for PWM signal. Calculate for minimum Vin and Max Vout operation.

May need to be higher to allow for transient condition during steps in load.

Use Doubler Driver: Mark box when using 2 powerstages per phase in doubler configuration.

Enable Tracking mode: LoopB tracks loopA

Close button: close window without saving

Read from Device Button: Restore the values entered to values before they got changed or read from a device if connected.

Write button: save the settings